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# Inventor Information for 09/977798

Inventor Name	City	State/Country
POULSEN, DAVID K.	CHAMPAIGN	ILLINOIS
SHAH, SANJIV M.	CHAMPAIGN	ILLINOIS
PETERSEN, PAUL M.	CHAMPAIGN	ILLINOIS
HAAB, GRANT E.	MAHOMET	ILLINOIS

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**Tables of Contents**

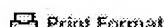
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*Min, S.L.; Baer, J.-L.;*

Parallel and Distributed Systems, IEEE Transactions on, Volume: 3, Issue: 1 1992

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[\[Abstract\]](#) [\[PDF Full-Text \(1560 KB\)\]](#) **IEEE JNL**

**2 False path elimination in quasi-static scheduling**

*Arrigoni, G.; Duchini, L.; Lavagno, L.; Passerone, C.; Watanabe, Y.;*

Design, Automation and Test in Europe Conference and Exhibition, 2002.

Proceedings, 4-8 March 2002

Pages:964 - 970

[\[Abstract\]](#) [\[PDF Full-Text \(282 KB\)\]](#) **IEEE CNF**

**3 Compilation of I/O communications for HPF**

*Coelho, F.;*

Frontiers of Massively Parallel Computation, 1995. Proceedings. 'Frontiers '95 Fifth Symposium on the, 6-9 Feb. 1995

Pages:102 - 109

[\[Abstract\]](#) [\[PDF Full-Text \(676 KB\)\]](#) **IEEE CNF**

**4 A simulation-based comparison of two reflective memory approach**

*Jovanovic, M.; Tomasevic, M.; Milutinovic, V.;*

System Sciences, 1995. Proceedings of the Twenty-Eighth Hawaii International Conference on, Volume: 1, 3-6 Jan. 1995

Pages:140 - 149 vol.1

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**5 Program analysis and transformations for fast data sharing**

*Ai Li; Hermannsson, G.; Wittie, L.;*

High Performance Distributed Computing, 1994., Proceedings of the Third IEE International Symposium on , 2-5 Aug. 1994

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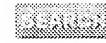
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**Tables of Contents**

- Journals & Magazines
- Conference Proceedings
- Standards

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  Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 The ParaScope parallel programming environment**

Cooper, K.D.; Hall, M.W.; Hood, R.T.; Kennedy, K.; McKinley, K.S.; Mellor-Crummey, J.M.; Torczon, L.; Warren, S.K.;  
Proceedings of the IEEE, Volume: 81, Issue: 2, Feb. 1993  
Pages:244 - 263

[\[Abstract\]](#) [\[PDF Full-Text \(1984 KB\)\]](#) [IEEE JNL](#)**2 Parallelizing applications into silicon**

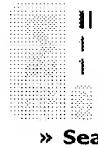
Babb, J.; Rinard, M.; Moritz, C.A.; Lee, W.; Frank, M.; Barua, R.; Amarasingh  
Field-Programmable Custom Computing Machines, 1999. FCCM '99. Proceedin  
Seventh Annual IEEE Symposium on, 21-23 April 1999  
Pages:70 - 80

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) [IEEE CNF](#)**3 Symbolic performance modeling of parallel systems**

van Gemund, A.J.C.;  
Parallel and Distributed Systems, IEEE Transactions on, Volume: 14, Issue: 2, Feb. 2003  
Pages:154 - 165

[\[Abstract\]](#) [\[PDF Full-Text \(386 KB\)\]](#) [IEEE JNL](#)**4 Interprocedural partial redundancy elimination with application to distributed memory compilation**

Agrawal, G.;  
Parallel and Distributed Systems, IEEE Transactions on, Volume: 9, Issue: 7 1998

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- Basic
- Advanced

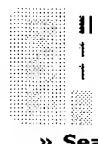
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- Home
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## Tables of Contents

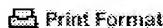
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- By Author
- Basic
- Advanced

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- Home
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- Conference Proceedings
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- By Author
- Basic
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<input type="checkbox"/>	L2	L1 and (memory with update with operation\$2)	240
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